

F.M.:15

(a) Parallel

SUBJECT: COMPUTER APPLICATION

ST. LAWRENCE HIGH SCHOOL

A JESUIT CHRISTIAN MINORITY INSTITUTION



CLASS: XI DATE: 15.06.2020

ANSWER KEY – 01 TOPIC – EVOLUTION OF COMPUTERS & COMPUTER ORGANIZATION

Choose the correct option: (1X15=15) 1) What is probably the oldest counting machine?: (b) Napier's Bone (c) Pascaline (a) Abacus (d) Difference Engine 2) The first device to help in multiplication & division was: (b) Napier's Bone (c) Pascaline (d) Difference Engine (a) Abacus 3) What device could do additions and subtractions in base 10?: (b) Napier's Bone (c) Pascaline (a) Abacus (d) Difference Engine 4) Who is regarded as the father of modern digital computers?: (a) Abacus (b) John Napier (c) Blaise Pascal (d) Charles Babbage 5) This is a set of instructions indicating to the computer the exact sequence of steps that must be followed to process a given set of data: (a) Program (b) Logic (c) Output (d) Code 6) Which was the first stored program electronic computer?: (a) EDSAC (b) EDVAC (c) Difference Engine (d) Analytic Engine 7) Which technology were used in first generation computers?: (a) Vacuum tubes (b) Transistors (c) ICs (d) Microprocessor 8) Transistors were introduced in which generation computer?: (a) First (b) Second (c) Third (d) Fourth 9) Machine independent high level programming languages were introduced in which generation of computers?: (a) First (b) Second (d) Fourth (c) Third network computing system is based on the working of the human brain: 10) The

(c) Dual

(d) Neural

(b) Serial

11) In which g	eneration of comput	ers, Integrated C	Circuits(IC) in	stead of transistors were us	ed?:
(a) First	(b) Second	(c) Th	ird	(d) Fourth	
12) The concept of LAN and WAN came into existence in which generation of computers?:					
(a) First	(b) Second	(c) Th	ird	(d) Fourth	
13) The technology that was build during 4th generation computers:					
(a) Vacuum tu	ibes (b) T	ransistors	(c) ICs	(d) Microprocessor	
14) Which ger	neration of computer	is hased on the	concent of r	parallel processing and neur	al
networks?:	iciation of compater	is bused on the	concept of p	araner processing and near	41
(a) Second	(b) First	(c) Fift	h	(d) Third	
15) contains up to hundred and thousand of components on a single chip:					
(a) SSI	(b) MSI	(c) VLSI	(d)	ULSI	
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