ST. LAWRENCE HIGH SCHOOL

A Jesuit Christian Minority Institution

WORKSHEET - 9

Topic – CU and Communication buses

Subject: COMPUTER SCIENCE   **Class - 11**   **F.M:15**

Chapter: Block diagram of computer   **Date: 26/06/2020**

Choose the correct answer for each question: [15 X 1 = 15]

1. Full form of CU:
   a. Central unit
   b. Centre unit
   c. Control unit
   d. None of the above

2. If the control signals are generated by combinational logic, then they are generated by a type of _____ controlled unit.
   a. Micro programmed
   b. Software
   c. Logic
   d. Hardwired

3. The Control unit of a computer _____.
   a. Stores data in the memory
   b. Accepts input data from the keyboard
   c. generates control signals to execute an instruction
   d. none of the above

4. X + ALU = CPU. What is X?
   a. Control unit
   b. Memory
   c. Input device
   d. Output device

5. _____ connects all major internal components to the CPU and memory.
   a. I/O devices
   b. Memory unit
   c. Buses
   d. ALU

6. Which of the following coordinates various operations using timing signal?
   a. I/O unit
   b. Memory unit
   c. ALU
   d. Control unit

7. _____ handles multiple tasks, such as fetching, decoding, execution handling and storing results.
    a. I/O unit
    b. Memory unit
    c. Control unit
    d. ALU

8. How many types of communication buses are there in a computer?
    a. Two
    b. Three
    c. Four
    d. One

9. Functions of control unit:
    a. It controls many execution units
    b. It interprets instructions.
    c. It controls data flow inside the processor.
    d. All of these

10. Which of the following does not process or store data?
    a. ALU
    b. Memory unit
    c. CU
    d. None of these

11. Data is transmitted from one part of a computer to another with the help of:
    a. CU
    b. Buses
    c. Memory unit
    d. ALU

12. Which of the following carries data among the memory unit, the I/O devices, and the processor?
    a. Data bus
    b. Address bus
    c. Control bus
    d. None of these

13. Which of the following carries the address of data (not the actual data) between memory and processor?
    a. Data bus
    b. Address bus
    c. Control bus
    d. None of these

14. _____ carries control commands from the CPU (and status signals from other devices) in order to control and coordinate all the activities within the computer.
    a. Data bus
    b. Address bus
    c. Control bus
    d. None of these

15. The _____ the bus width, faster would be the data flow on the data bus.
    a. Wider
    b. Thinner
    c. independent
    d. None of these

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